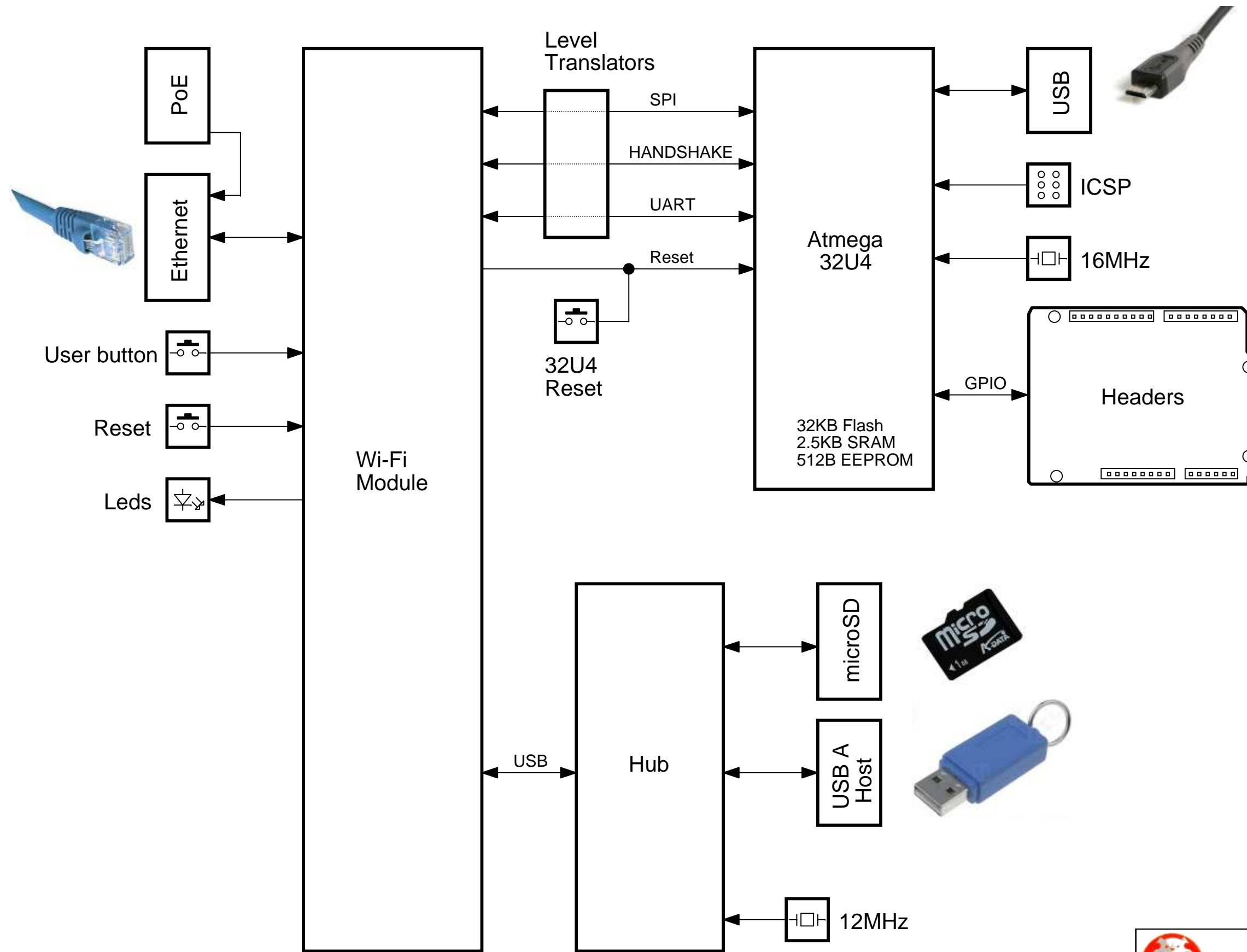



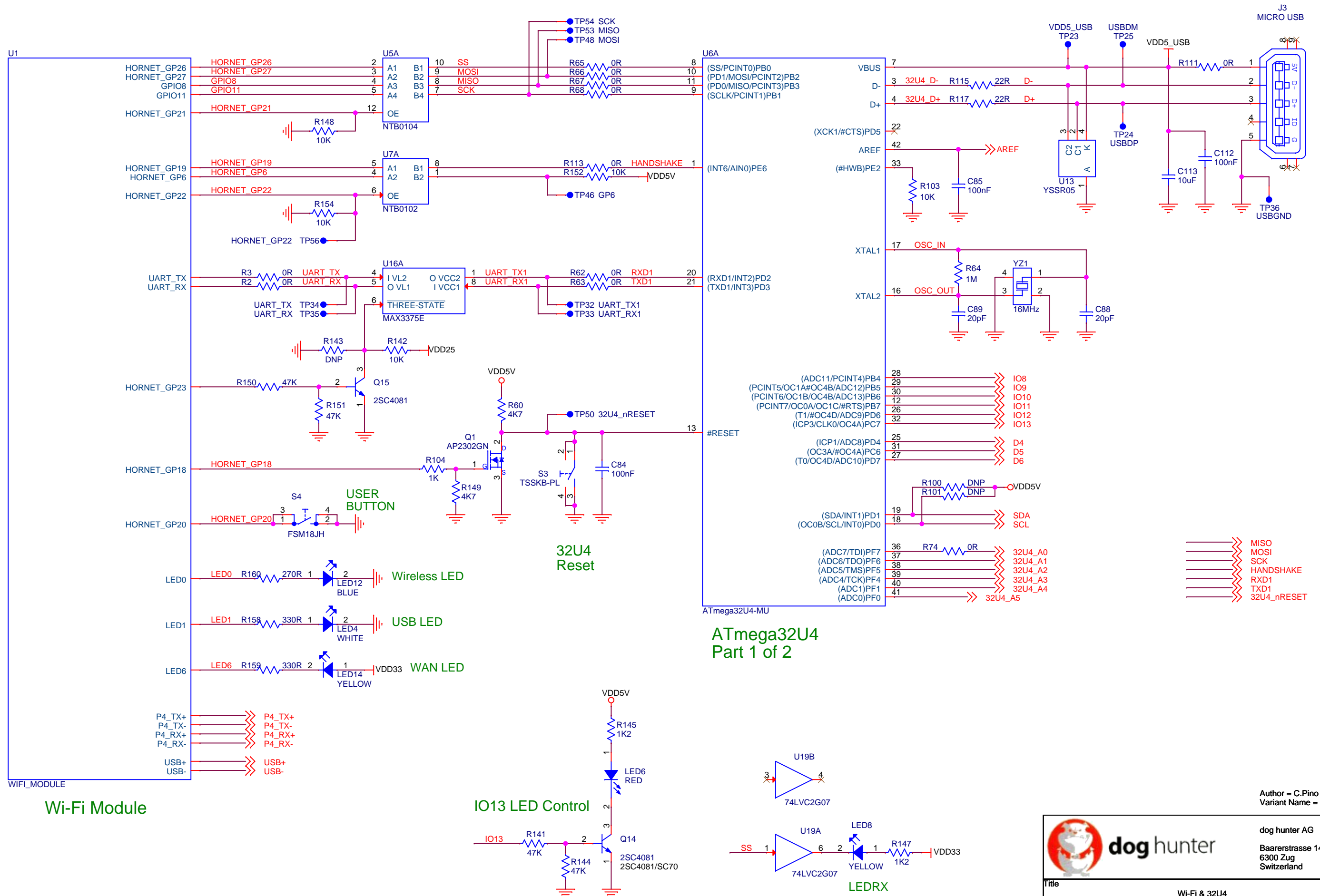
Block Diagram



Author = C.Pino
Variant Name = <Core Design>

 dog hunter		dog hunter AG Baarerstrasse 14 6300 Zug Switzerland	
		Title Block Diagram	
Size A3	Document Number Yun	Rev 4	
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Wi-Fi & 32U4



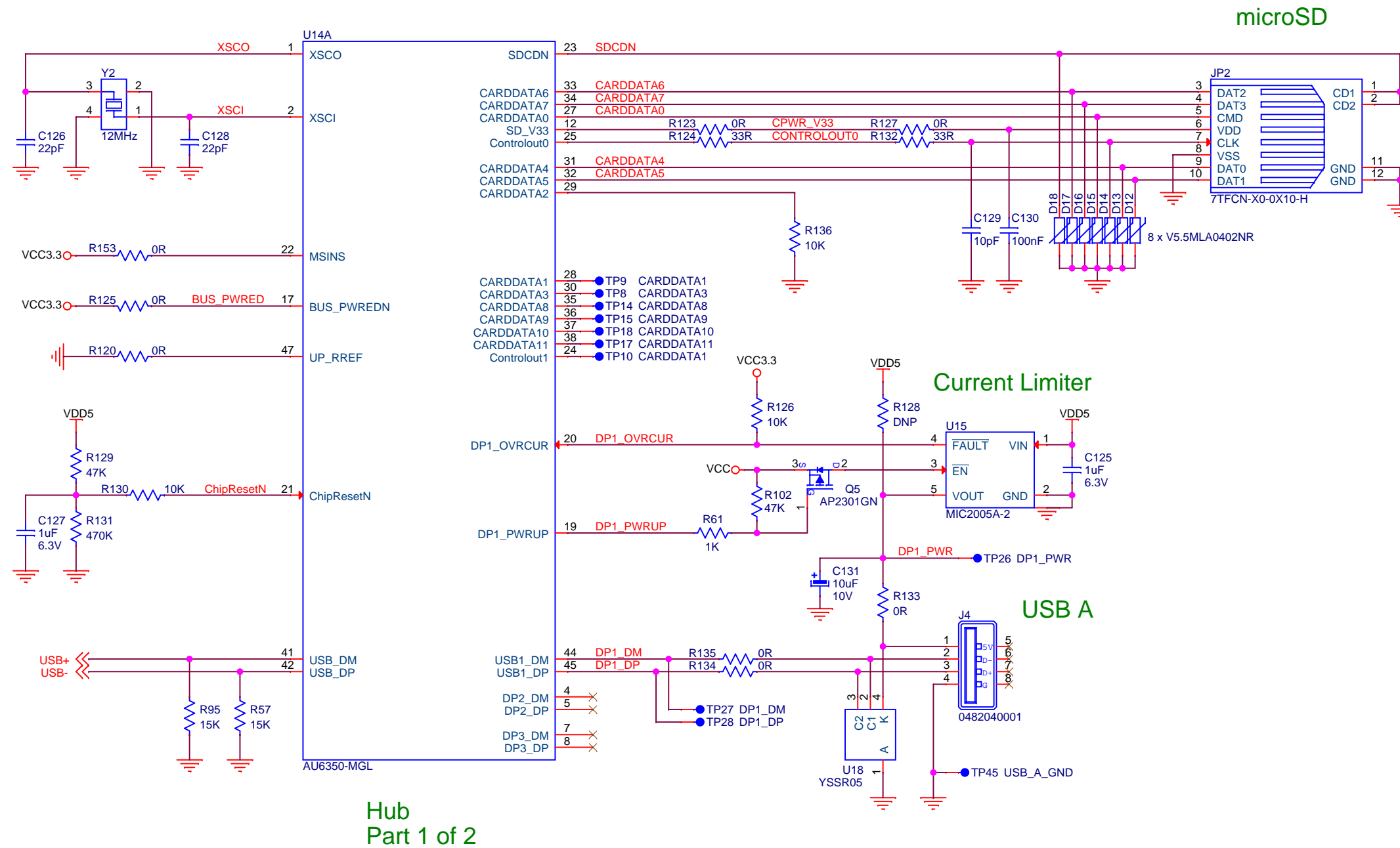
ATmega32U4 Part 1 of 2

Author = C.Pino
Variant Name =



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Hub, microSD & USB Host



Hub
Part 1 of 2

USB signal line trace:
 1.Keep traces of USB bus D+ and D- the same length.
 2.Achieve 90 ohm differential characteristic impedance.
 3.Achieve 45 ohm common characteristic impedance.
 4.Maintain parallelism between D+ and D-.
 5.Do not route USB2.0 D+ and D- over the power plane split.
 6.Do not route USB2.0 D+ and D- over the other high frequency signals.
 7.It is preferred to route USB2.0 D+ and D- over ground layer.
 8.It is preferred to route USB2.0 D+ and D- using single layer.

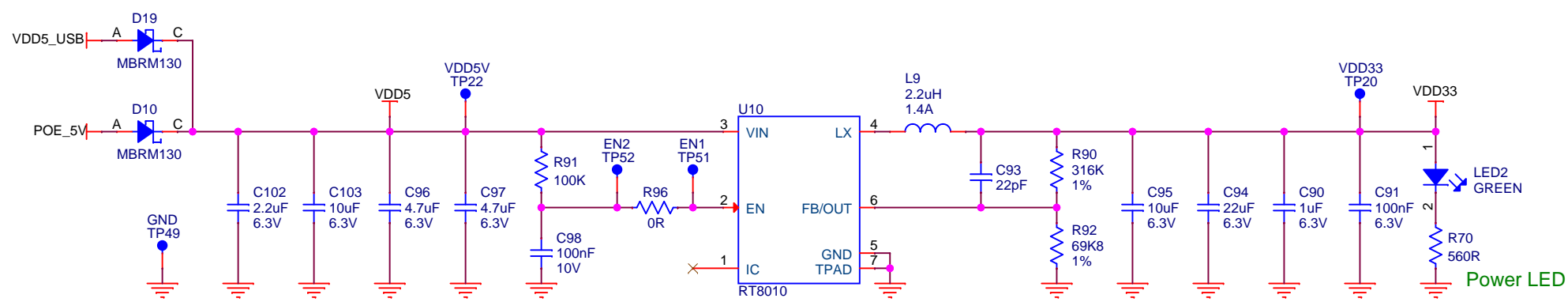
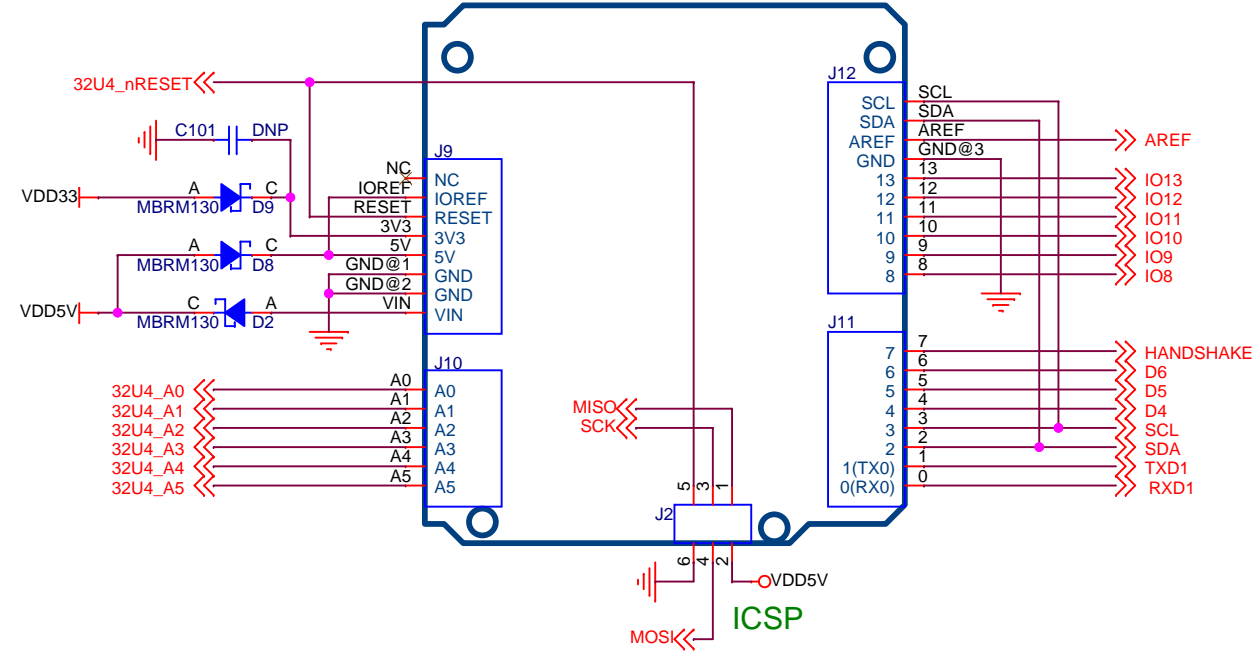
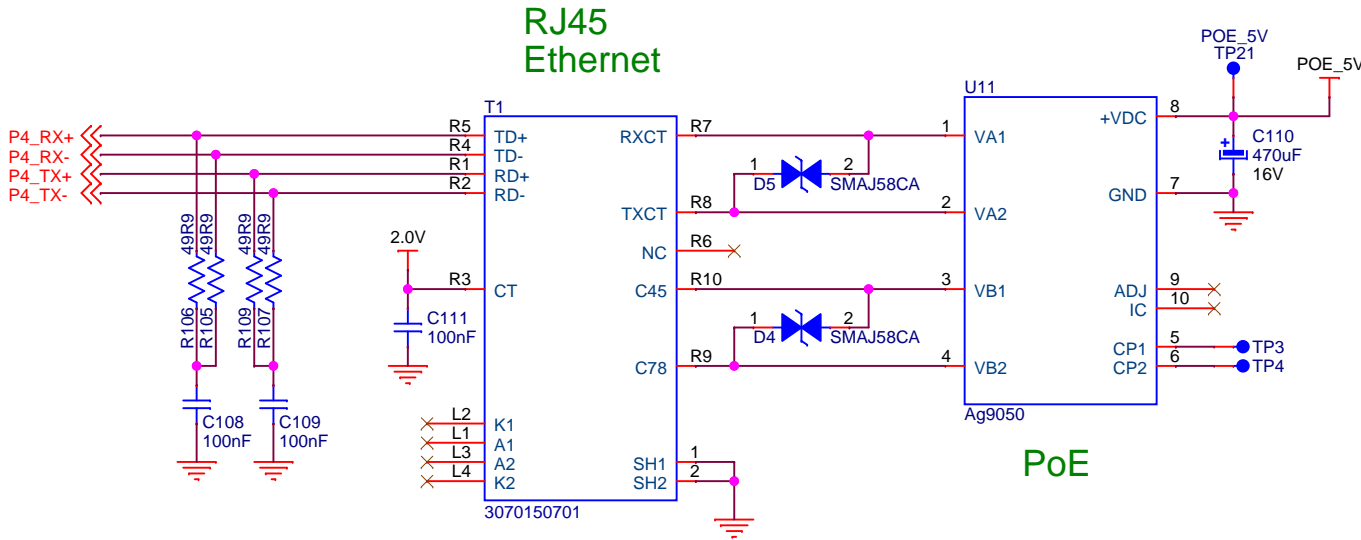
Author = C.Pino
 Variant Name =




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 Switzerland

Title		
Hub, microSD & USB Host		
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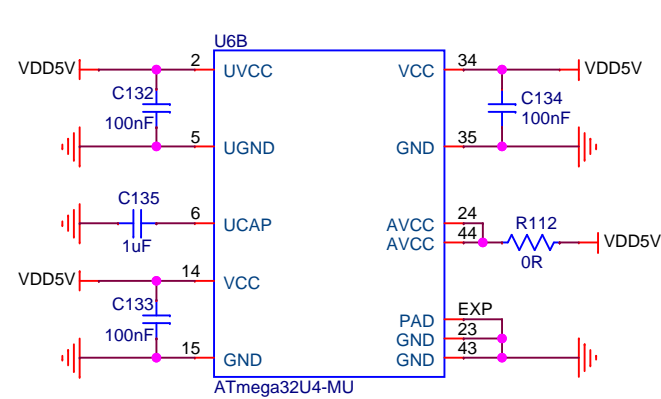
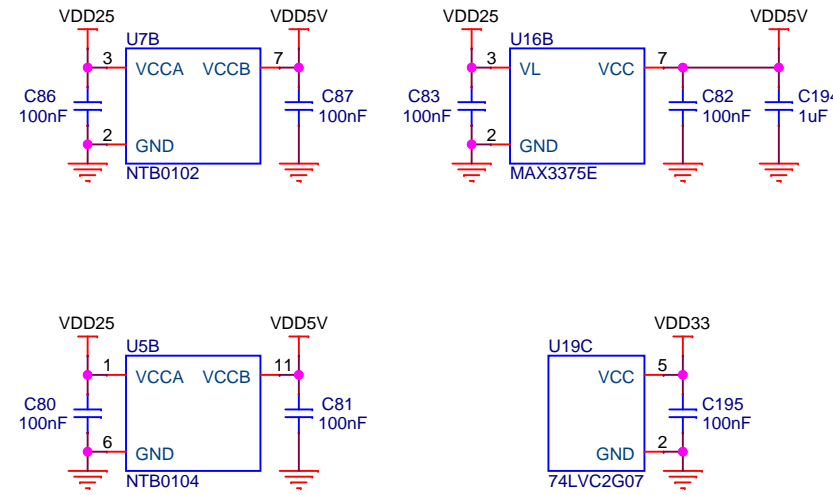
Power Supply & Pin Headers



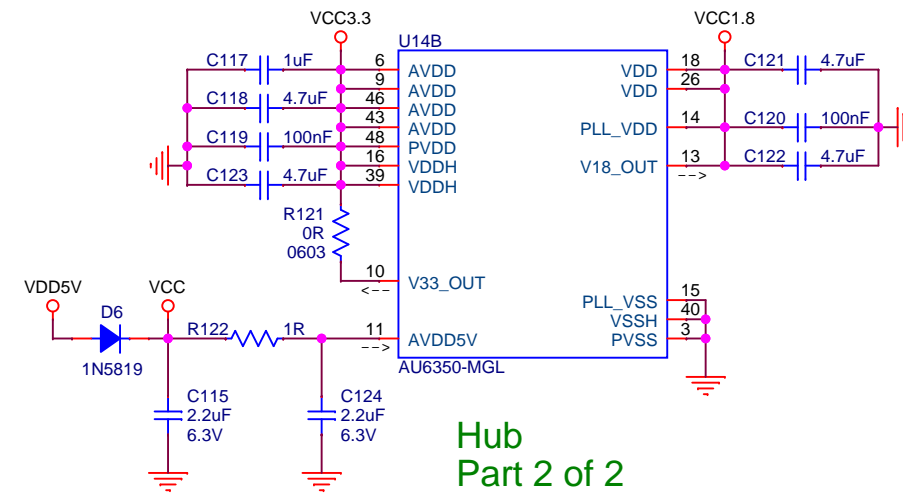
Author = C.Pino
Variant Name =

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		Baarerstrasse 14 6300 Zug Switzerland	
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Date: Friday, February 28, 2014	Sheet 4	of 5	

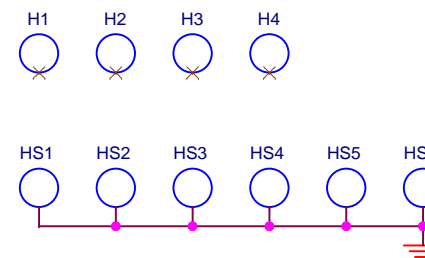
IC Power



ATmega32U4
Part 2 of 2



Hub
Part 2 of 2



Filter capacitors must be placed as close as possible to relative pin, as drawn in the schematic

Author = C.Pino
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